

**IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE**

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Applicant(s): Katsura HIRAI

Filed : CONCOMITANTLY HEREWITH

For : ELECTRICAL CIRCUIT, THIN
FILM TRANSISTOR, PRODUCTION
METHOD OF ELECTRICAL CIRCUIT,
AND PRODUCTION METHOD OF
THIN FILM TRANSISTOR

Art Unit :
Examiner :

Docket No. : 05170/HG

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**REQUEST FOR PUBLICATION OF
ASSIGNMENT INFORMATION**

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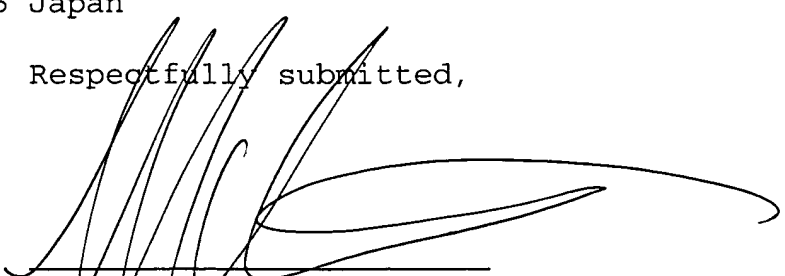
S I R :

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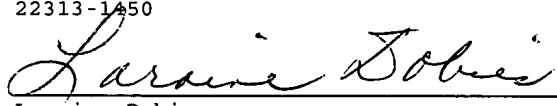
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Respectfully submitted,


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Laraine Dobies

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